

ABSTRACT

The recording pulse generator performs plural signal processing with one delay line, with regard to the control of recording signals in writing pit data onto the surface of a disk.

The recording pulse generator includes a first delay line connected to the common power supply line with a PLL oscillator having a second delay line, having plural circuit elements equivalent to the second delay line cascaded in multiple stages, a means that generates plural clocks each having different phase differences with a clock inputted to the first stage, according to the number of stages of the plural circuit elements of the first delay line, a means that selects an arbitrary clock from plural clocks generated, and a recording pulse generation means that generates a recording pulse whose pulse width is controlled on the basis of a clock selected.